

III B.Tech I Sem (E.C.E)

23A04501T	<u>ANALOG AND DIGITAL IC APPLICATIONS</u>	L	T	P	C
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Course Objectives:

1. To introduce the classification of Integrated Circuits, internal blocks and characteristics of Op-Amp.
2. To analyse linear and non-linear applications of Op-Amp .
3. To gain knowledge on active filters, timers and phased locked loops.
4. To understand the working of Voltage Regulators and Converters.
5. To study about different types of Digital ICs and their applications.

Course Outcomes:**At the end of the course, the students will be able to**

1. Understand the classification of Integrated Circuits, internal blocks and characteristics of Op-Amp.
2. Analyse linear and non-linear applications of Op-Amp .
3. Gain knowledge on active filters, timers and phased locked loops.
4. Understand the working of Voltage Regulators and Converters.
5. Know about different types of Digital ICs and their applications.

UNIT I ICs and OP- AMPS

Integrated Circuits and Operational Amplifier: Introduction, Classification of IC's, IC chip size and circuit complexity, basic information of Op-Amp IC741 and its features, the ideal Operational amplifier, Op-Amp internal circuit, Op-Amp characteristics - DC and AC, Features of 741 Op-Amp.

UNIT II Applications of OP- AMP

Linear Applications of Op-Amp: Inverting, non-inverting, Differential amplifiers, adder, subtractor, Instrumentation amplifier, AC amplifier, V to I and I to V converters, Integrator and differentiator.

Non-Linear Applications of Op-Amp: Sample and Hold circuit, Log and Antilog amplifier, multiplier and divider, Comparators, Schmitt trigger, Multi vibrators, Triangular and Square waveform generators, Oscillators.

UNIT III Active Filters and other ICs

Active Filters: Introduction, Butterworth filters – 1st order, 2nd order low pass and high pass filters, band pass, band reject and all pass filters.

Timer and Phase Locked Loops: Introduction to IC 555 timer, description of functional diagram, monostable and a stable operations and applications, Schmitt trigger, PLL - introduction, basic principle, phase detector/comparator, voltage controlled oscillator (IC 566), low pass filter, monolithic PLL and applications of PLL.

UNIT IV Voltage Regulators and Converters

Voltage Regulator: Introduction, Series Op-Amp regulator, IC Voltage Regulators, IC 723 general purpose regulators, Switching Regulator.

D to A and A to D Converters: Introduction, basic DAC techniques - weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, A to D converters - parallel comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC, DAC and ADC Specifications.

UNIT V Digital ICs

CMOS Logic: CMOS logic levels, MOS transistors, Basic CMOS Inverter, NAND and NOR gates, CMOS AND-OR-INVERT and OR-AND-INVERT gates, implementation of any function using CMOS logic.

Combinational Logic IC's: Specifications and Applications of TTL-74XX & CMOS 40XX Series ICs - Code Converters, Decoders, Encoders, Priority Encoders, Multiplexers, Demultiplexers, Parallel Binary Adder/ Subtractor, Magnitude Comparators.

Sequential Logic IC's: Familiarity with commonly available 74XX & CMOS40XX Series ICs - All Types of Flip-flops, Synchronous Counters, Decade Counters, Shift Registers.

Textbooks:

1. D. Roy Choudhury, Shail B. Jain, "Linear Integrated Circuit", 4th edition (2012), New Age International Pvt.Ltd., New Delhi, India
2. Floyd, Jain, "Digital Fundamentals", 8th edition (2009), Pearson Education, New Delhi.

References:

1. Ramakant A. Gayakwad, "OP-AMP and Linear Integrated Circuits", 4th edition (2012), Prentice Hall / Pearson Education, New Delhi.
2. Sergio Franco (1997), Design with operational amplifiers and analog integrated circuits, McGraw Hill, New Delhi.
3. Gray, Meyer (1995), Analysis and Design of Analog Integrated Circuits, Wiley International, New Delhi.